



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/690,530	10/23/2003	Takayuki Kondo	117478	6381
25944	7590	10/20/2005		
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			EXAMINER HEALY, BRIAN	
			ART UNIT 2883	PAPER NUMBER

DATE MAILED: 10/20/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

Priority

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Allowable Subject Matter

Claims 15,16 and 18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. None of the references of record teaches or suggests a light scattering mechanism composed of a resin into which a light scattering particle is mixed (claim 15); that the light scattering mechanism is composed of a resin of which a surface is processed to include an irregularity (claim 16); that the light scattering mechanism being composed of at least one of a resin and a glass in which a light scattering particle is dispersed and being dome shaped (claim 18).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claims 1-14,17,19 and 20 are rejected under 35 U.S.C. 102(e) as being anticipated by Yoshimura et. al., U.S.P. No. 6,611,635.

Yoshimura 736' teaches (Figs.1-153) an optical integrated interconnection multiwavelength circuit between chips (Note that the optical modules/chips will inherently include timing chips and driving chips or other processors which are commonly used in high speed optical operations) comprising: an optical substrate 12 upon which is placed plural optical waveguides 24 (optical waveguides can be of differing widths, lengths and heights) , at least first and second light emitting/receiving devices 626,628 which are at least partially covered by the waveguide 24 and are in contact with the waveguide and the waveguide and devices are held by a polymeric cladding/ adhesive 23 wherein the first and second light emitting/receiving devices are interconnected via electrodes 27,27x which also serve as bonding/wiring pads for other optical chips/modules , which clearly, fully meets Applicant's claimed limitations.

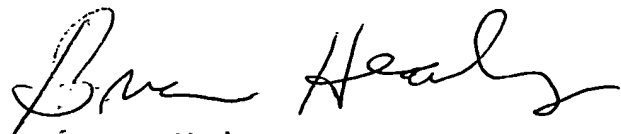
The following references are also cited by the Examiner as being pertinent prior art: Bureau et. al., U.S.P. No. 5,262,351 (Figs.1-8b) and Harada, U.S.P. Patent Application Publication No. US 2003/0007736A1 (Figs.1-12).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian M. Healy whose telephone number is (571)272-2347. The examiner can normally be reached on Compressed schedule Tues.-Thurs. 7AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Frank Font can be reached on (571)272-2415. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Brian M. Healy
Primary Examiner
Art Unit 2883



Brian Healy
Primary Examiner